

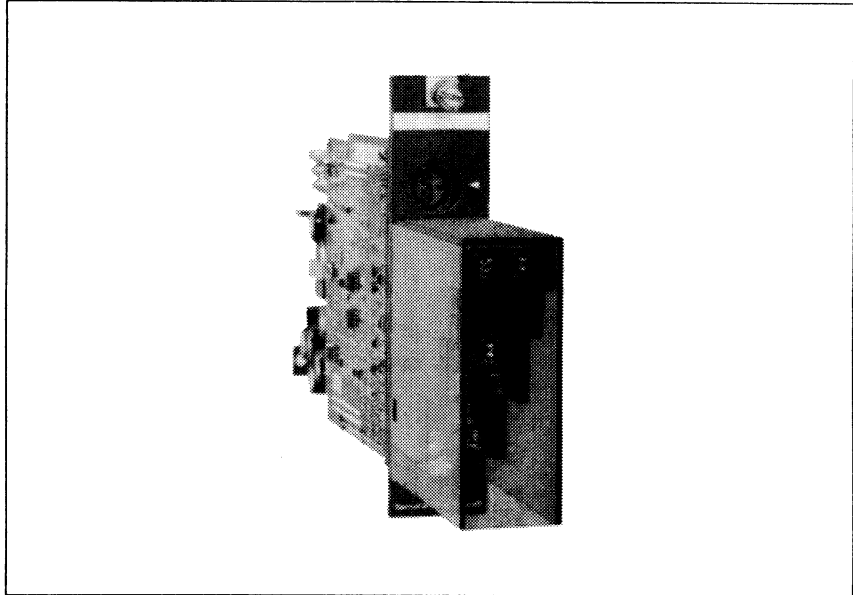
SystempaK (Digital/File Type) Single Input Arithmetic Relay Module Model J-SCM 80/85

Introduction

The Single Input Arithmetic Relay Module (J-SCM 80/85) provides a variety of arithmetic operations which are selected with dedicated Handy Communicator in the field.

- Ratio / bias setting
- Square root extraction
- Signal reversing
- Function generating
- Limiting
- Rate of change limiting
- Lead / lag
- Differentiating operation

The J-SCM 80/85 provides one-input and two-input modules, and the square root operation can be combined to any arithmetic operations.



Functions

Arithmetic operation	Description
Ratio / bias setting	An input signal is ratioed, and addition or subtraction biased for outputting it.
Square root extraction	Square root extraction is performed for an input signal.
Signal reversing	Reversing an input signal for outputting it.
Function generating	Approximating an input signal with 20 linearization point for outputting it.
Limiting	Limits an output signal when an input signal exceeds the upper or lower limit value previously set.
Rate of change limiting	Limits an output signal when rate of input signal change exceeds the upper or lower limit value previously set.
Lead / lag	Activating as lead or lag element for the change of input signal.
Differentiating operation	Differentiating operation is applied to an input signal.

Specification

Items		Ratio / Bias setting	Square root extraction	Signal reversing	Function generating	
Input	Input signal	1 to 5V DC or 4 to 20mA DC				
	Input bias current	-1μA or less				
Output	Output signal	No.1 Output	1 to 5V DC or 4 to 20mA DC (No.1 output must be 1 to 5V DC when connecting the signal with the A-MC I/O cable.)			
		No.2 Output	1 to 5V DC (Between No.1 and No.2 output is not isolated.)			
	Output impedance	Voltage output: 250Ω or less, Current output: 250kΩ or more				
	Allowable load resistance	0 to 600Ω (current output)				
Arithmetic equation		$V_0 - 1 = K(V_i - 1) + B$ ($V_0 = \text{Output}$, $V_i = \text{Input}$, $K = \text{Ratio}$, $B = \text{Bias}$)	$V_0 - 1 = 2\sqrt{V_i - 1}$ ($V_0 = \text{Output}$, $V_i = \text{Input}$)	$V_0 = 6 - V_i$ ($V_0 = \text{Output}$, $V_i = \text{Input}$)	$V_0 = f(V_{in})$ $f(V_{in})$ is a linearization approximation.	
Setting range		K: - 6.999 to +7.999 B: - 699.9 to +799.9%	Dropout: 0 to 20%FS of input	—	Linearization point setting range $X_n: - 6.9\% \leq X_n \leq 106.9\%$ $Y_n: - 6.9\% \leq Y_n \leq 106.9\%$	
Number of linearization lines		—	—	—	20 (21 linearization points)	
Accuracy	Arithmetic operation	No.1 Output (Note 1)	±0.25%FS (for ratio 1 and bias 0%)	±0.25%FS (10 to 100% input) ±2.0%FS (0 to less than 10% input) 0±0.5%FS (at dropout)	±0.25%FS	±0.25 × G%FS (G: Slope G > Partition of 1.0)
		No.2 Output	±0.5%FS (added to No.1 output accuracy.)			
	Setting	—	Dropout setting accuracy ±0.1%FS	—	—	
Common	Current	24V DC $+10\%$ -15%				
	Current consumption	200mA or less (at 24V)				
	Ambient temperature	5 to 45°C				
	Ambient humidity	0 to 90%RH				
	Mounting	File				
	Front mask	Black				
	Weight	300g				
Operating influence		Supply voltage effect: ±0.2%FS/24V DC $+10\%$ -15%				
		Temperature effect: ±0.2%FS/10°C				

Note: 1) For No. 1 current output, adds 0.1% to those indicated above.

Items		Limiting	Rate of change limiting	Lead / Lag	Differentiating operation
Input	Input signal	1 to 5V DC or 4 to 20mA DC			
	Input bias current	-1μA or less			
Output	Output signal	No.1 Output	1 to 5V DC or 4 to 20mA DC (No.1 output must be 1 to 5V DC when connecting the signal with the A-MC I/O cable.)		
		No.2 Output	1 to 5V DC (Between No.1 and No.2 output is not isolated.)		
	Output impedance	Voltage output: 250Ω or less, Current output: 250kΩ or more			
	Allowable load resistance	0 to 600Ω (current output)			
Arithmetic equation		—	—	$V_0 - 1 = \frac{1 + T_1 S}{1 + T_2 S} (V_i - 1) + B$	$V_0 - 1 = \frac{T_1 S}{1 + T_2 S} (V_i - 1) + B$
Setting range		Limit value: -6.9 to +106.9%FS (both upper and lower limits)	Rate of change: -699.9 to +799.9%FS/min.	Lead time: 0 to 99.99 minutes Lag time: 0 to 99.99 minutes	
Number of linearization lines		—	—	—	—
Accuracy	Arithmetic operation	No.1 Output (Note 1)	±0.25%FS (other than limiting operation range)	±0.25%FS (other than limiting operation range)	±0.25%FS
		No.2 Output	±0.5%FS (Added to No.1 output accuracy.)		
	Setting	±0.1%FS	±0.5%FS/min.	Time constant: ±0.5 minutes (at setting 1 min.) , Bias: ±0.1%FS	
Common	Current	24V DC $\pm \frac{10}{15}$ %			
	Current consumption	200mA or less (at 24V)			
	Ambient temperature	5 to 45°C			
	Ambient humidity	0 to 90%RH			
	Mounting	File			
	Front mask	Black			
	Weight	300g			
Operating influence		Supply voltage effect: ±0.2%FS/24V DC $\pm \frac{10}{15}$ %			
		Temperature effect: ±0.2%FS/10°C			

Note: 1) For No. 1 current output, adds 0.1% to those indicated above.

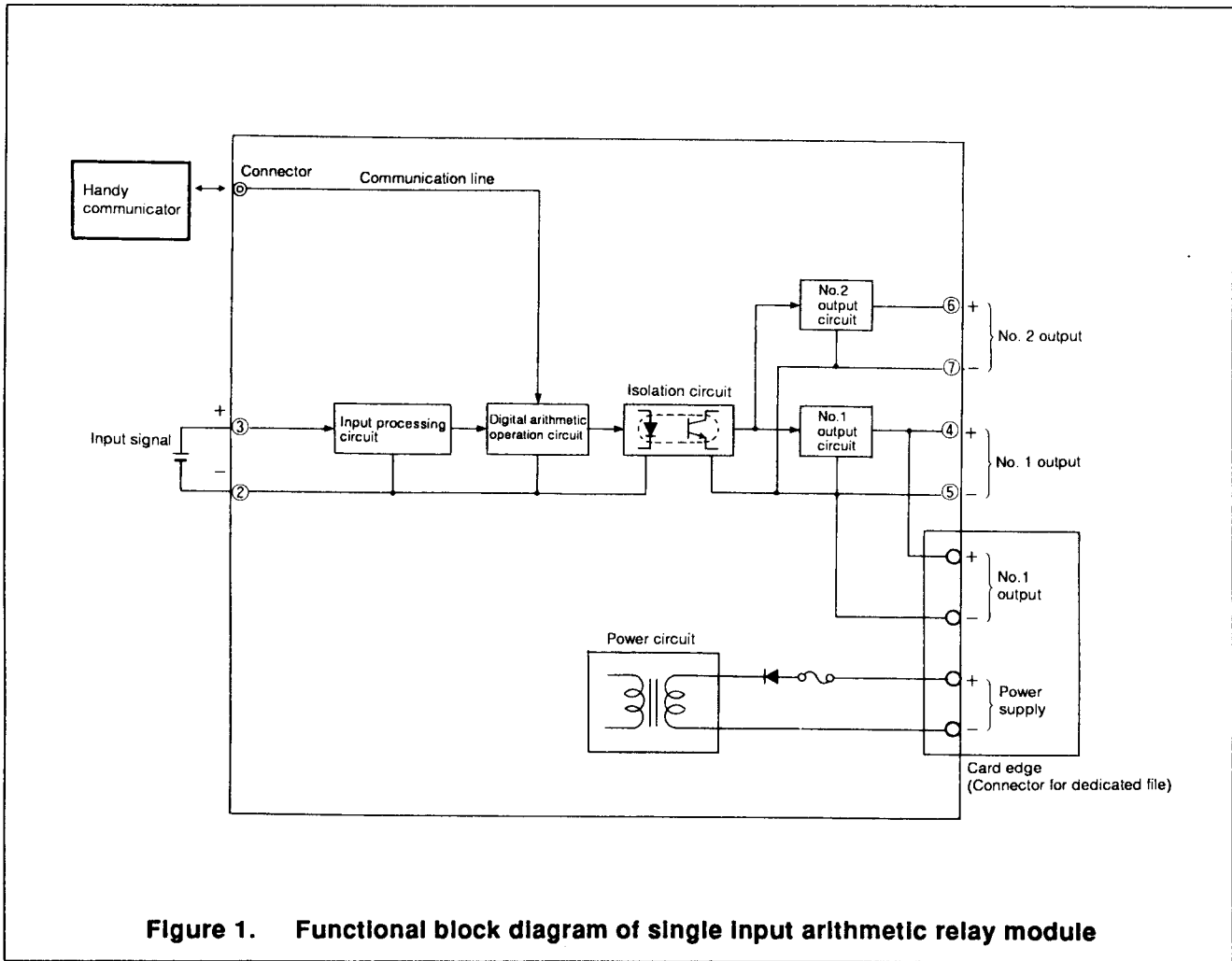


Figure 1. Functional block diagram of single input arithmetic relay module

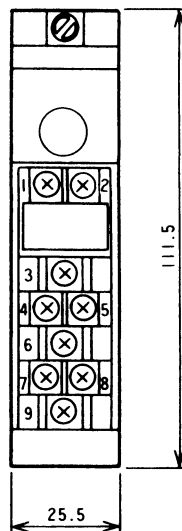
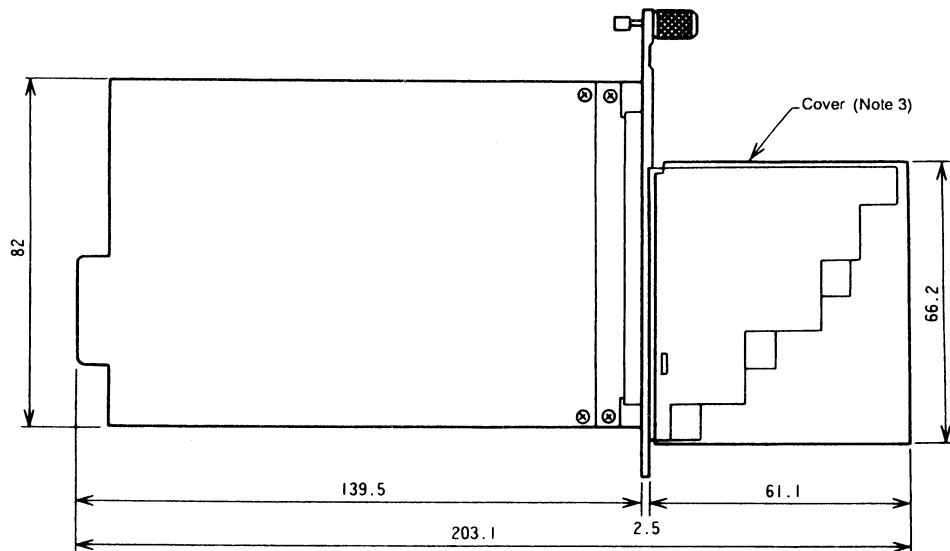
Model Number Table

One-output module

Basic model number	Selections		Description
	I	II	
J-SCM 80			Single Input Arithmetic Relay Module (1-output)
	-1		Input 1 to 5V DC
	-2		Input 4 to 20mA DC
		1	Output 1 to 5V DC
		2	Output 4 to 20mA DC

Two-output module

Basic model number	Selections		Description
	I	II	
J-SCM 85			Single Input Arithmetic Relay Module (2-output)
	-1		Input 1 to 5V DC
	-2		Input 4 to 20mA DC
		1	No.1 output 1 to 5V DC No.2 output 1 to 5V DC
		2	No.1 output 4 to 20mA DC No.2 output 1 to 5V DC



No.	Description
1 (Note 1)	---
2 (Note 1)	Input (-)
3	Input (+)
4	No. 1 Output (+)
5	No. 1 Output (-)
6	No. 2 Output (+) (Note 2)
7	No. 2 Output (-) (Note 2)
8	---
9	GND

- Note 1) 250Ω resistor is added for current input.
 2) For two-output mode.
 3) Operate the Module with a cover.
 4) Terminal screws: M3.5
 5) Use the pressured terminals with insulation sheath.

Figure 2. Dimensions and wiring diagram

Ordering Information

When ordering, please specify:

- 1) Tag number
- 2) Arithmetic functions
Ratio bias setting, Square root extraction, Signal reversing, Function generating,
Limiting, Rate of change limiting, Lead/lag, Differentiating operation
- 3) Setting data
For ratio bias setting; Ratio value, Bias value
For function generating; Linearization point data
For limiting; Lower limit value, Upper limit value
For rate of change limiting; Rate of minus change limit value, Rate of plus change limit value
For lead/lag; Lead time (T1), Lag time (T2), Bias value
For differentiating operation; Lead time (T1), Lag time (T2), Bias value

Specifications are subject to change without notice.

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Yamatake (Thailand) Co., Ltd.	: Thailand	66-2-210-0900~7
Yamatake Philippines, Inc.	: Philippines	63-2-817-6452
PT. Yamatake Berca Indonesia	: Indonesia	62-21-230-5537/5539
Yamatake Controls Singapore Pte. Ltd.	: Singapore	65-778-5966
YCV Corporation	: U.S.A.	1-602-548-1800

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